

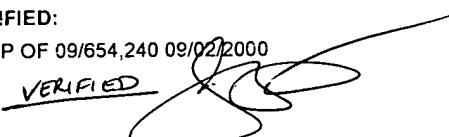
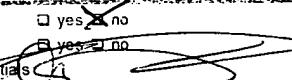
33872 U.S. PRO

106/572

P

**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM 10077189	FILING DATE 02/15/2002	CLASS 326	SUBCLASS 14	GAU 2819	EXAMINER V. F. Thompson
**APPLICANTS: Liu Tong; Lien Jung-Cheun; Feng Sheng; Huang Eddy; Sun Chung-Yuan; Liao Naihui; Xiong Weidong;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A CIP OF 09/654,240 09/02/2000 					
** FOREIGN APPLICATIONS VERIFIED: 					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input checked="" type="checkbox"/>		RESCIND <input type="checkbox"/>		ATTORNEY DOCKET NO	
Foreign priority claimed		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ACT-322	
35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no			
Verified and Acknowledged Examiner's initials 					
TITLE : Freeway gate array architecture					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Fig.s.Drwg.
TERMINAL		PREPARED FOR ISSUE		Application Examiner
DISCLAIMER		<p>WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.</p>		

FILED WITH: DISK (CRF) CD-ROM
(Attached in pocket on right inside flap)